

FIG. 1

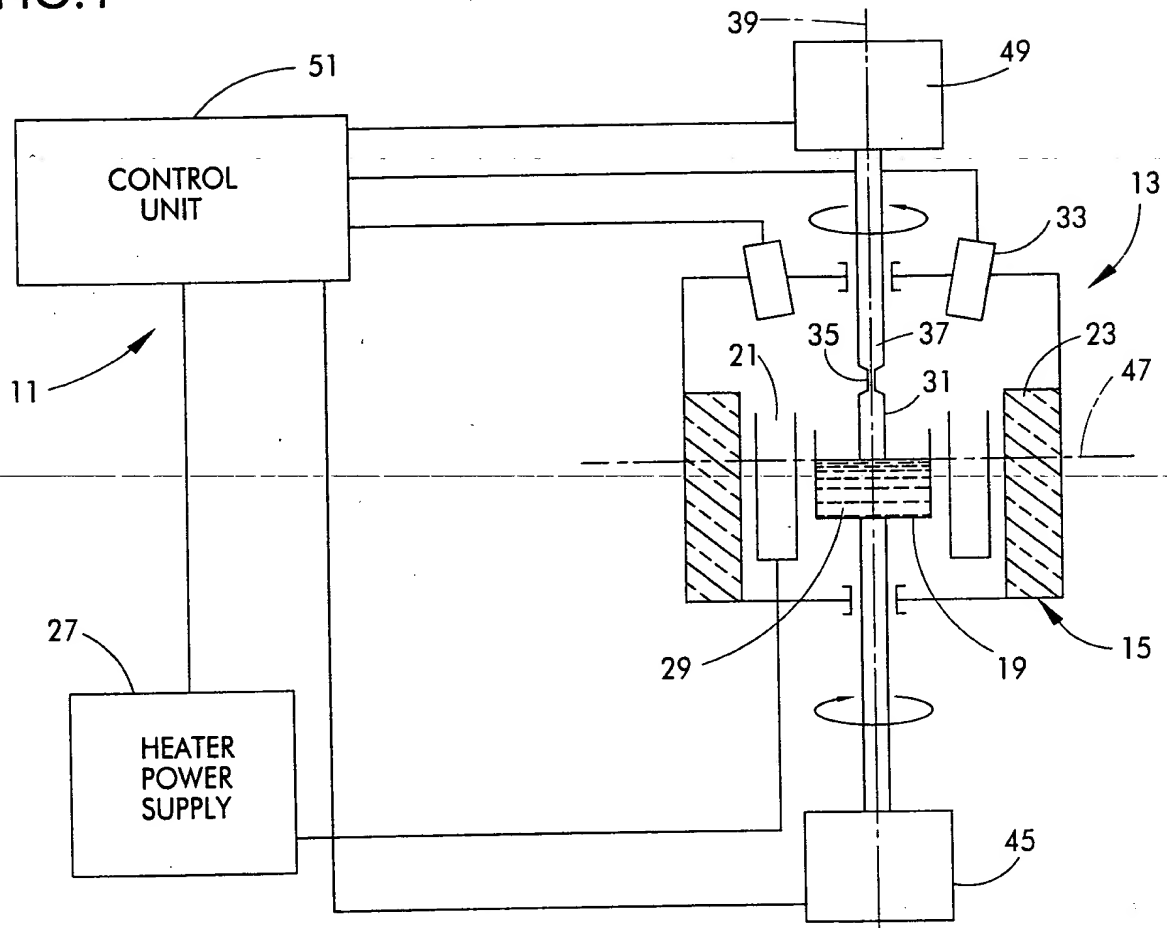


FIG.2

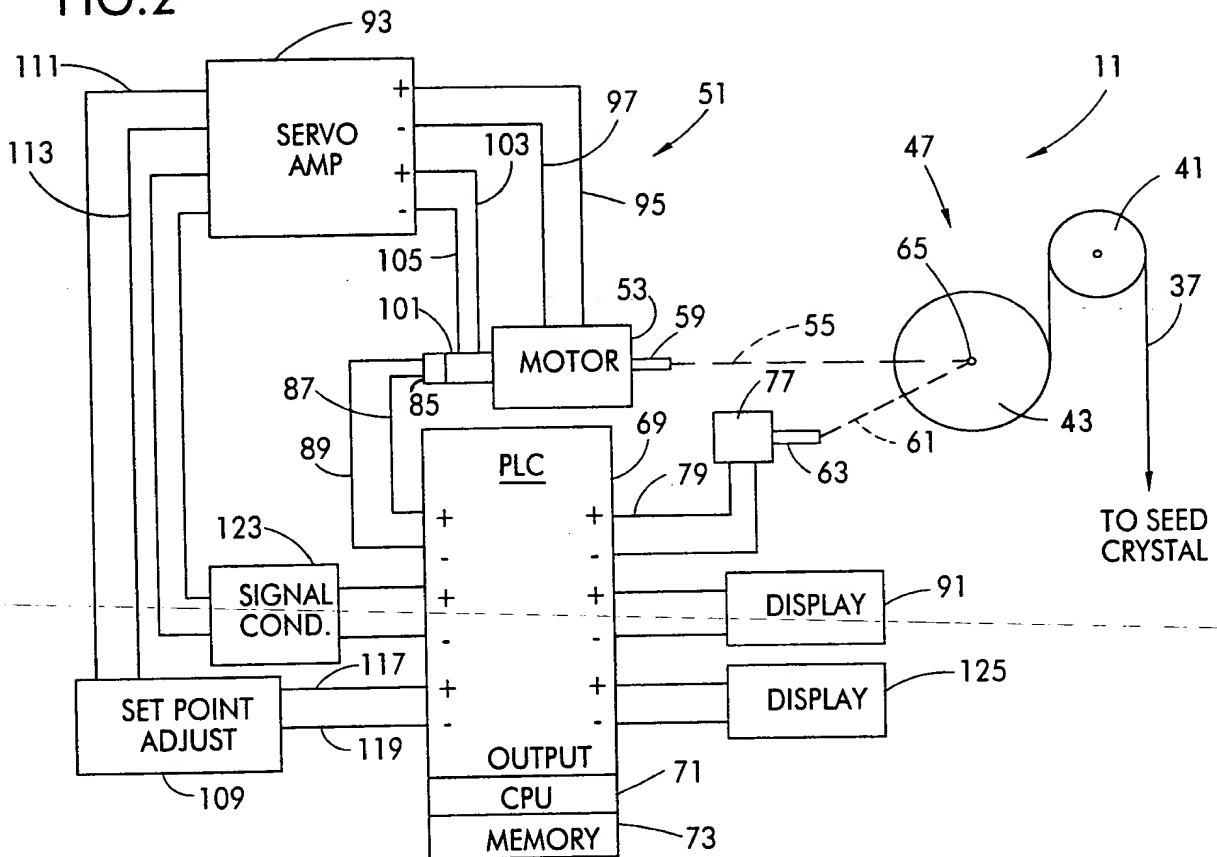


FIG.5

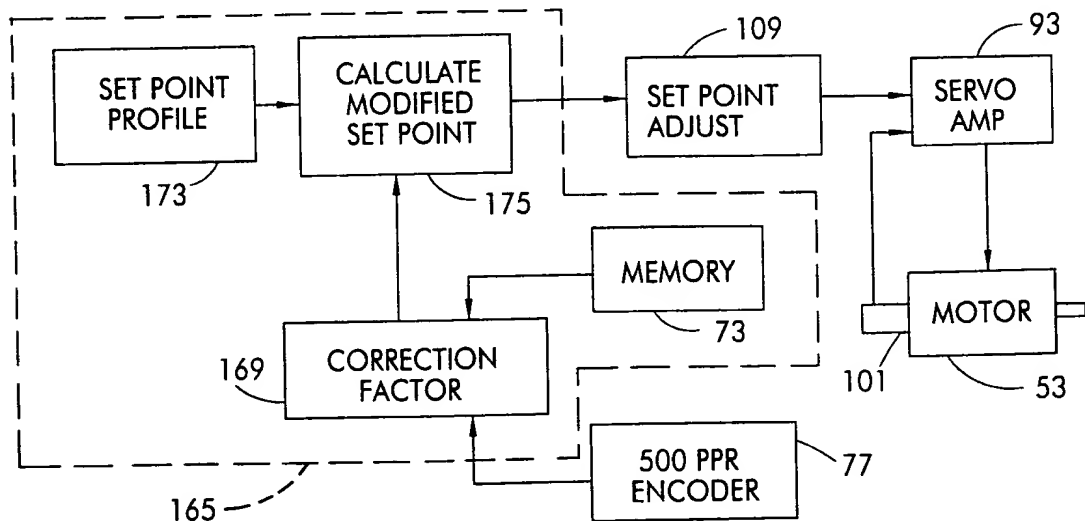
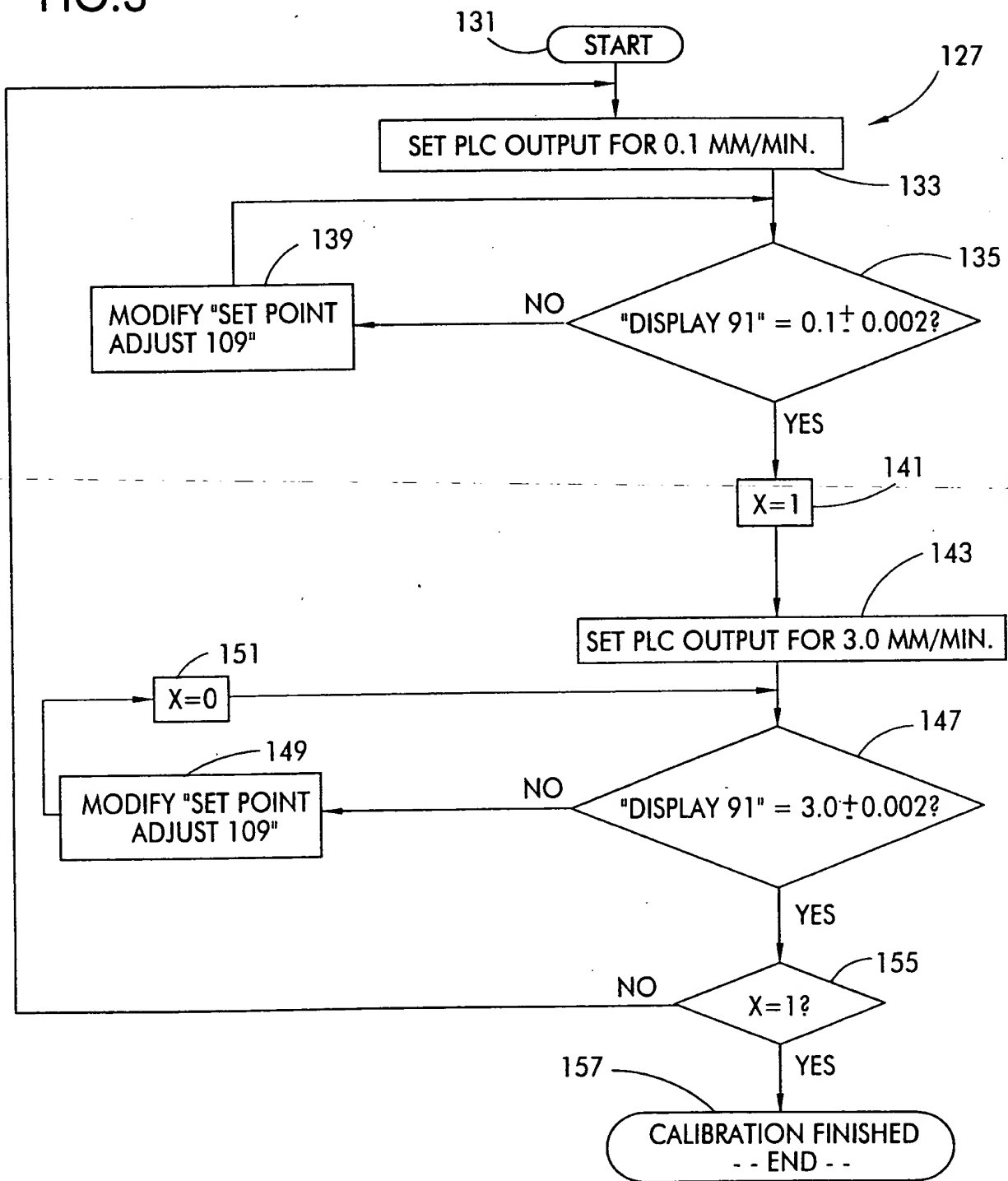


FIG.3



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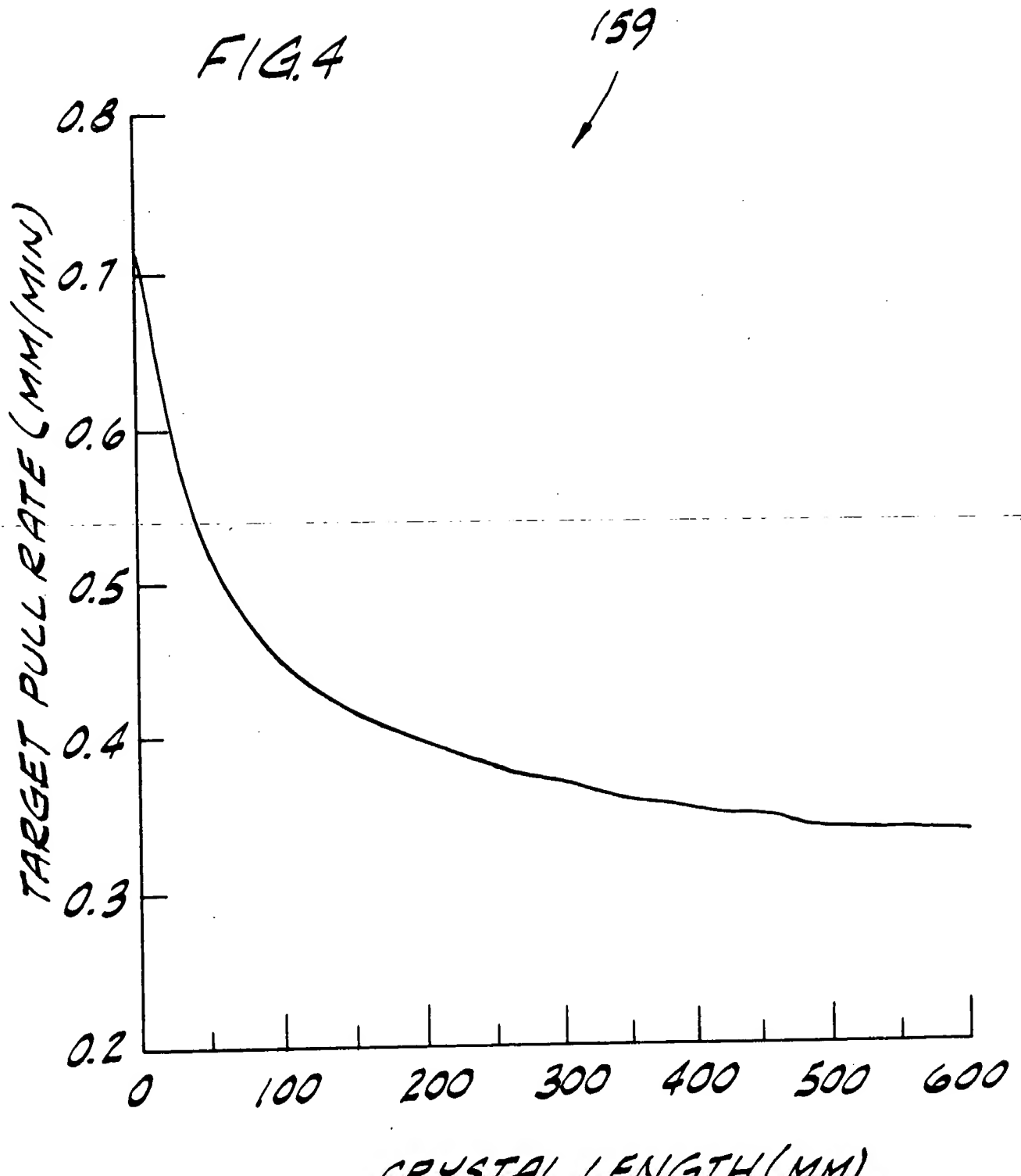


FIG.6A

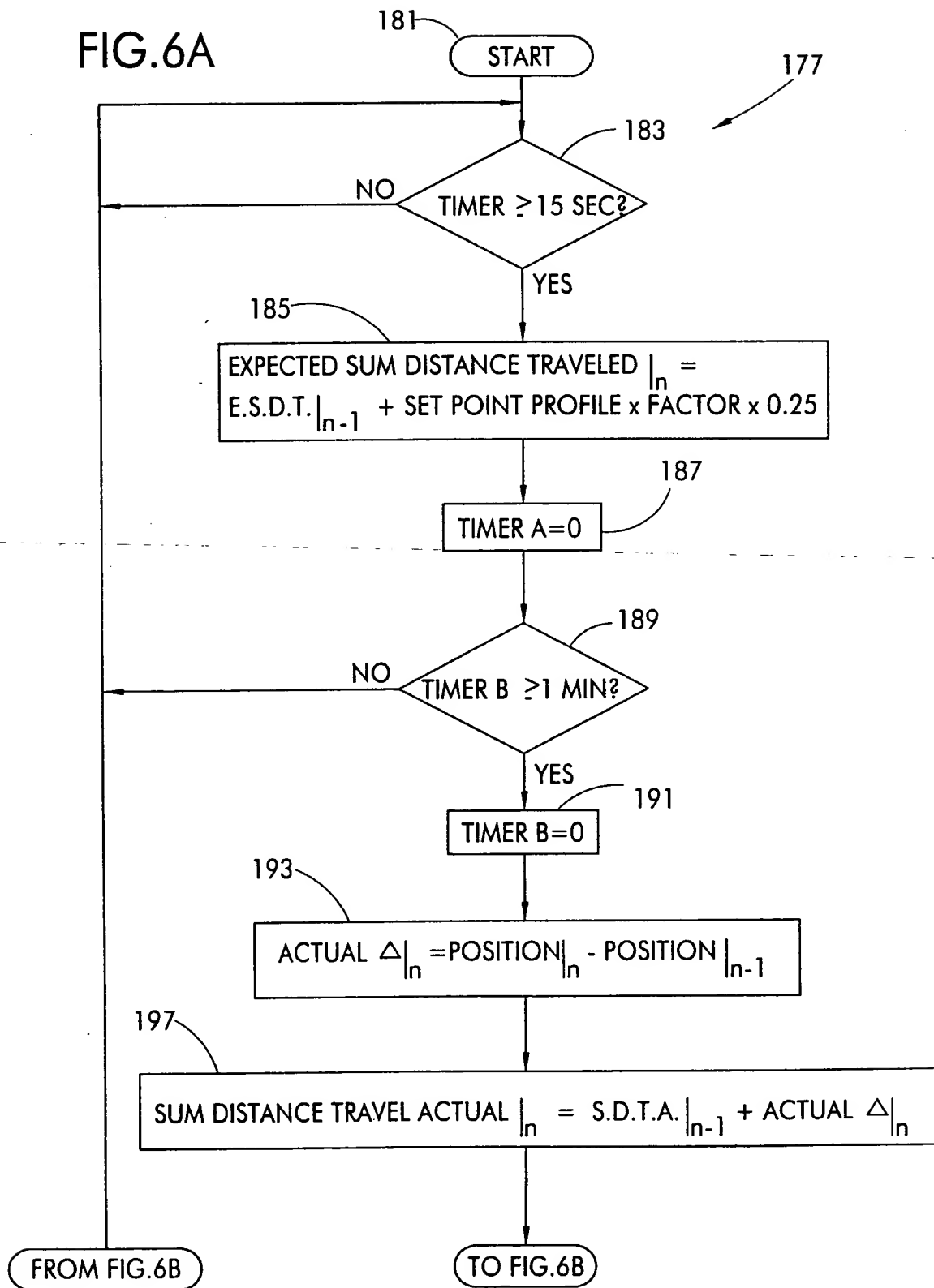
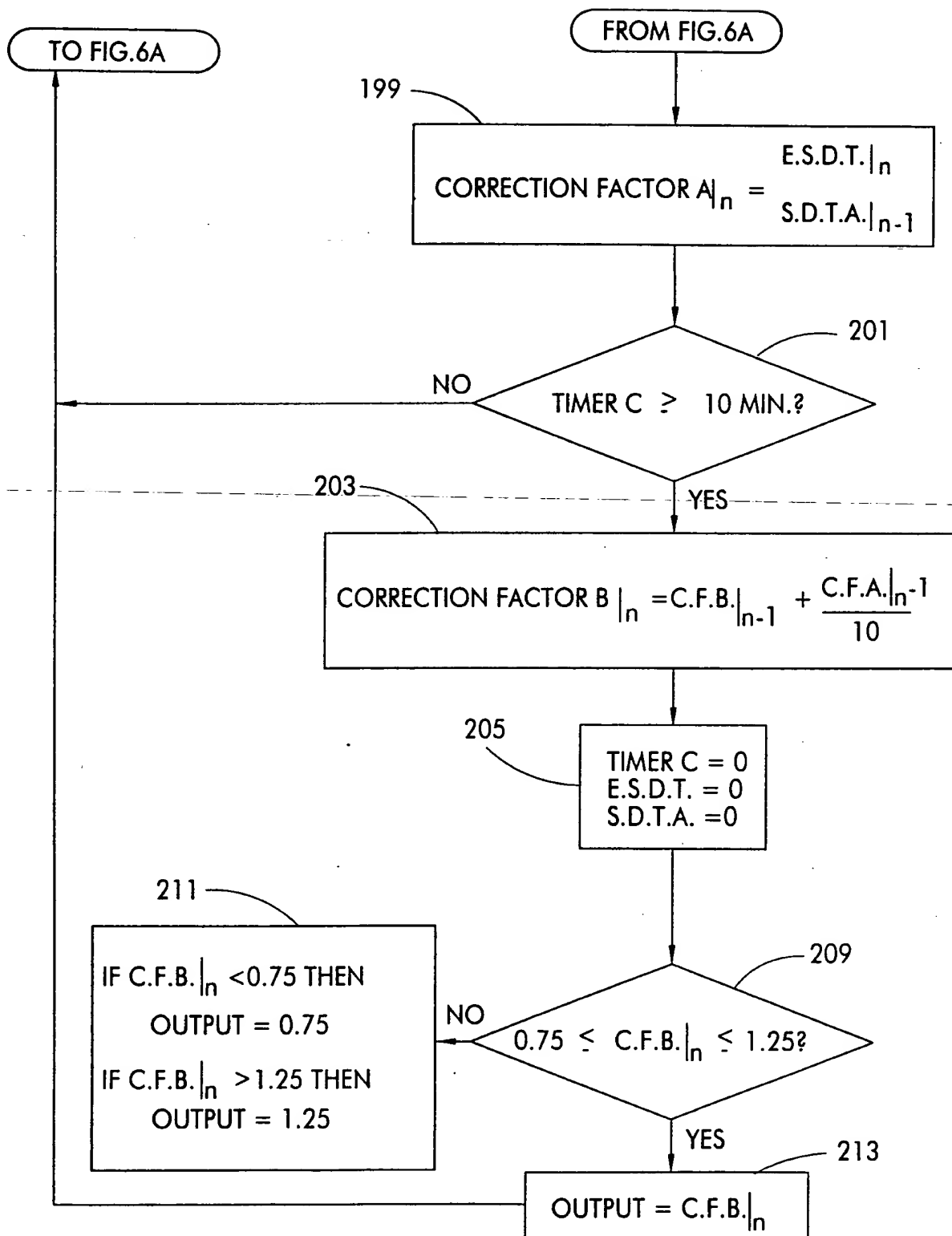
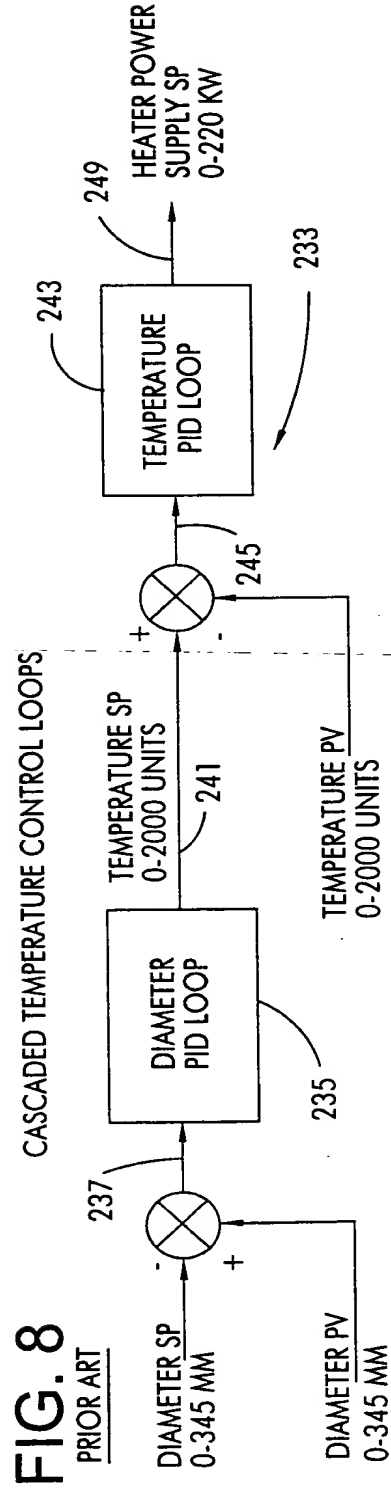
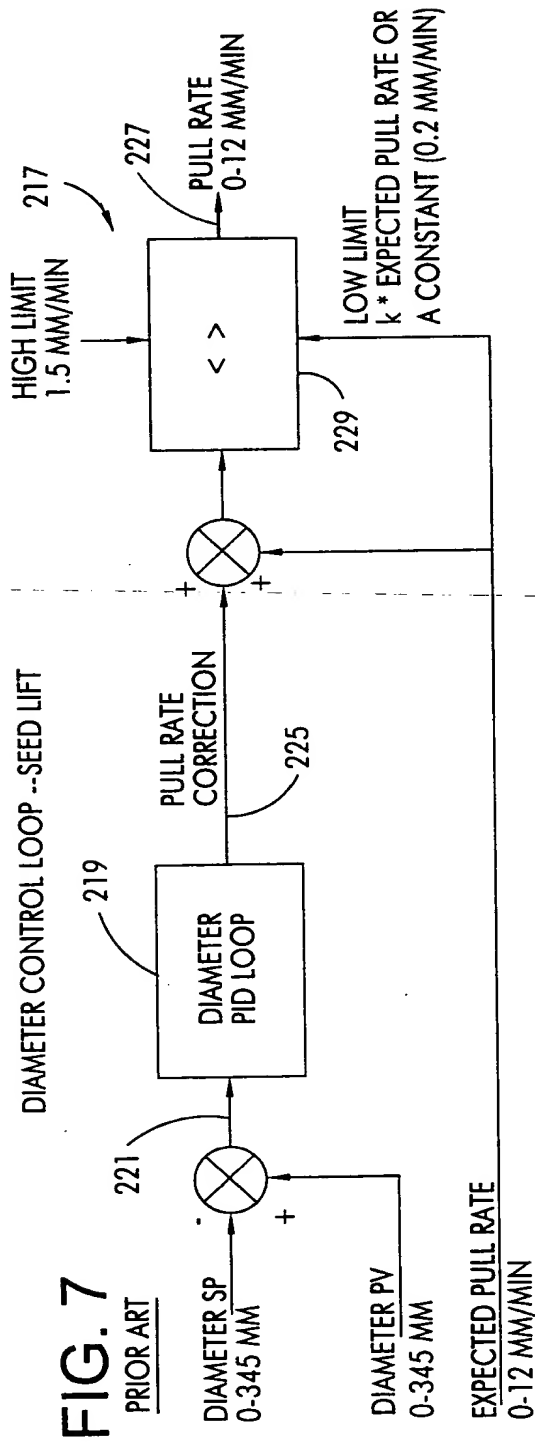


FIG. 6B





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FIG. 9

PRIOR ART

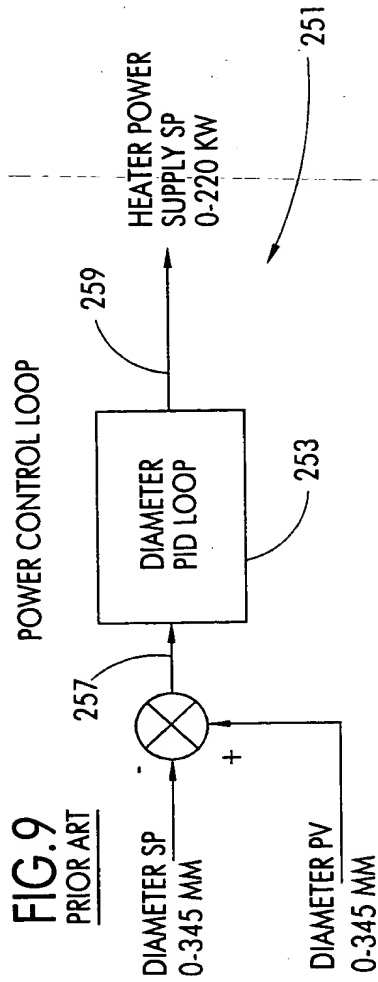
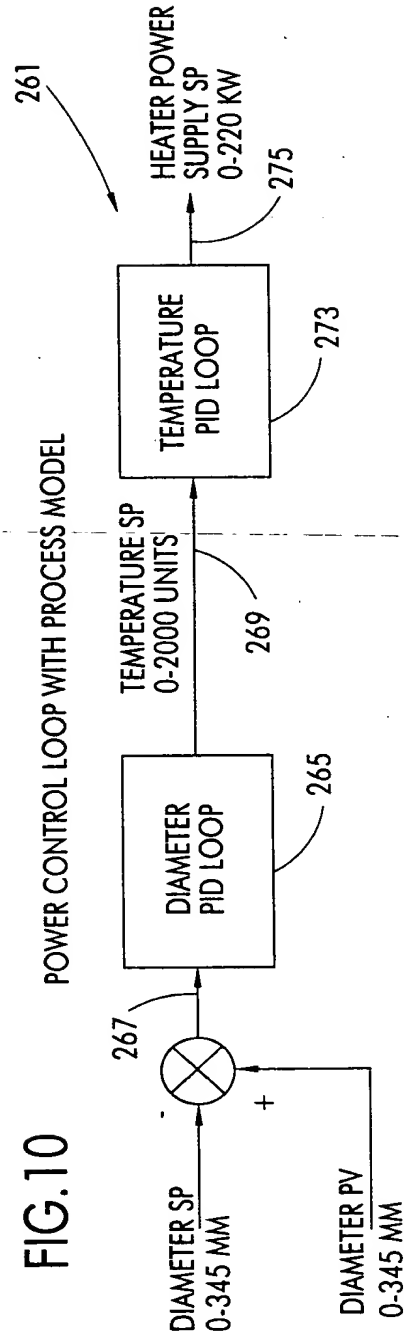


FIG. 10

POWER CONTROL LOOP WITH PROCESS MODEL



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FIG.11

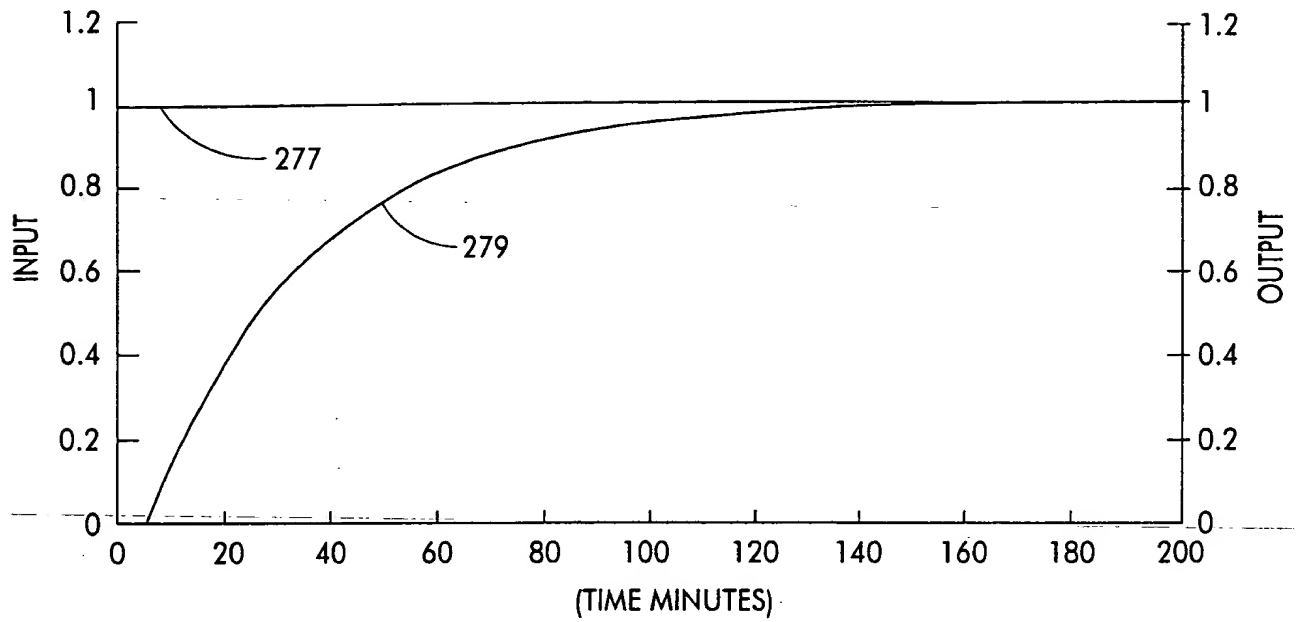
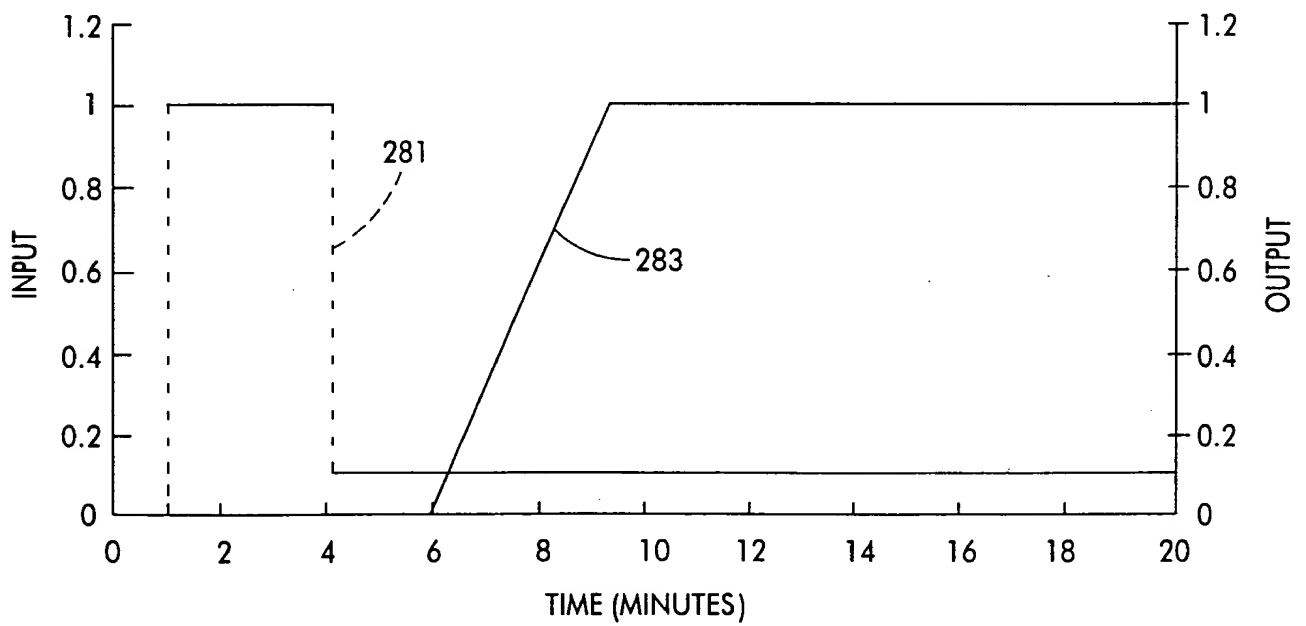


FIG.12



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FIG. 13

